



Gokaraju Rangaraju Institute of Engineering & Technology (Autonomous)
 Name of the college & Code : Gokaraju Rangaraju Institute of Engineering & Technology, 24
 Name of the PG Program : Master of Technology
 Specialization : VLSI (2019 Admitted Batch)
 Academic Year & Semester : 2019-20, II Semester

wef: 03 February 2020								
I Year - II Semester								
Day/Hour	9:00 - 10:00	10:00 - 11:00	11:00 - 12:00	12:00-1:00	1:00 - 2:00	2:00 - 3:00	3:00 -4:00	Room No.
MONDAY	Design for Testability	Analog CMOS IC Design	Mini-Project/ Faculty Meeting	Lunch	Analog CMOS IC Design Lab			LAB :2104
TUESDAY	Analog CMOS IC Design	Digital Signal Processors and Architecture	Mini-Project/ Faculty Meeting		Pedagogy Studies	Design for Testability	Mini-Project/ Faculty Meeting	Class : 2102
WEDNESDAY	Analog CMOS IC Design	ASIC Design	Mini-Project/ Faculty Meeting		Analog CMOS IC Design Lab			
THURSDAY	ASIC Design	Digital Signal Processors and Architecture	Mini-Project/ Faculty Meeting		ASIC Design Lab			
FRIDAY	Digital Signal Processors and Architecture	ASIC Design	Mini-Project/ Faculty Meeting		ASIC Design Lab			
SATURDAY	ASIC Design	Pedagogy Studies	Mini-Project/ Faculty Meeting		Mini-Project/Seminar			

S.No	Subject Code	Subject Name	Faculty Name
1	GR18D5086	Analog CMOS IC Design	Dr. Sreehari
2	GR18D5087	ASIC Design	Dr. D. Jayanthi
3	GR18D5090	Design for Testability	Dr. G. Mamatha
4	GR18D5091	Digital Signal Processors and Architecture	Dr. Asisa Kumar Panigarahy
5	GR18D5212	Pedagogy Studies	Mr. M. Sankara Rao
6	GR18D5094	Analog CMOS IC Design Lab	Dr. Sreehari
7	GR18D5095	ASIC Design Lab	Dr. D. Jayanthi

M.Tech Coordinator
Dr.V.Sreehari

HOD
Dr. N. Swetha

Principal
Dr.J.Praveen